

Processor Block Diagram

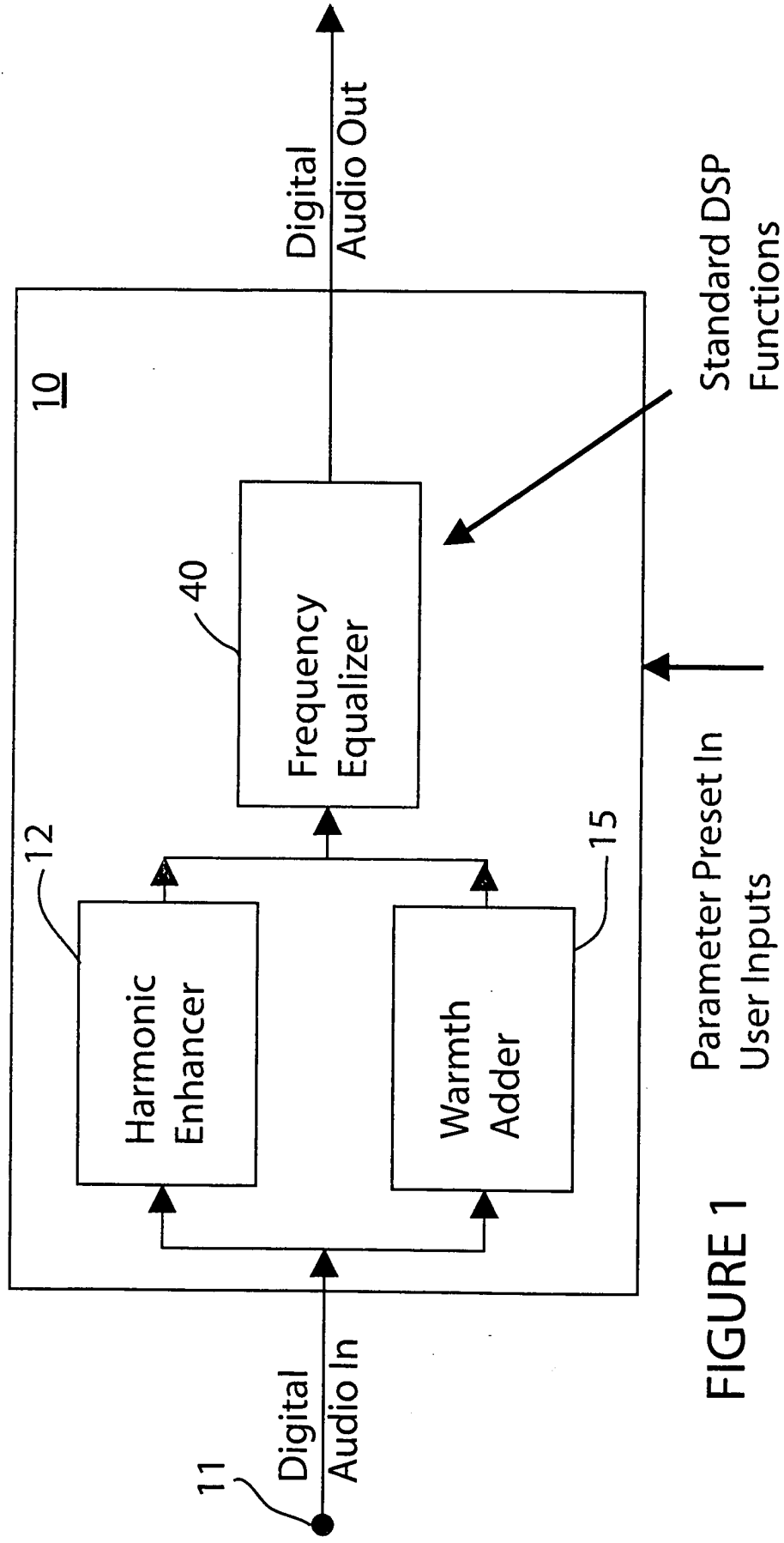


FIGURE 1



Harmonic Enhancer Diagram

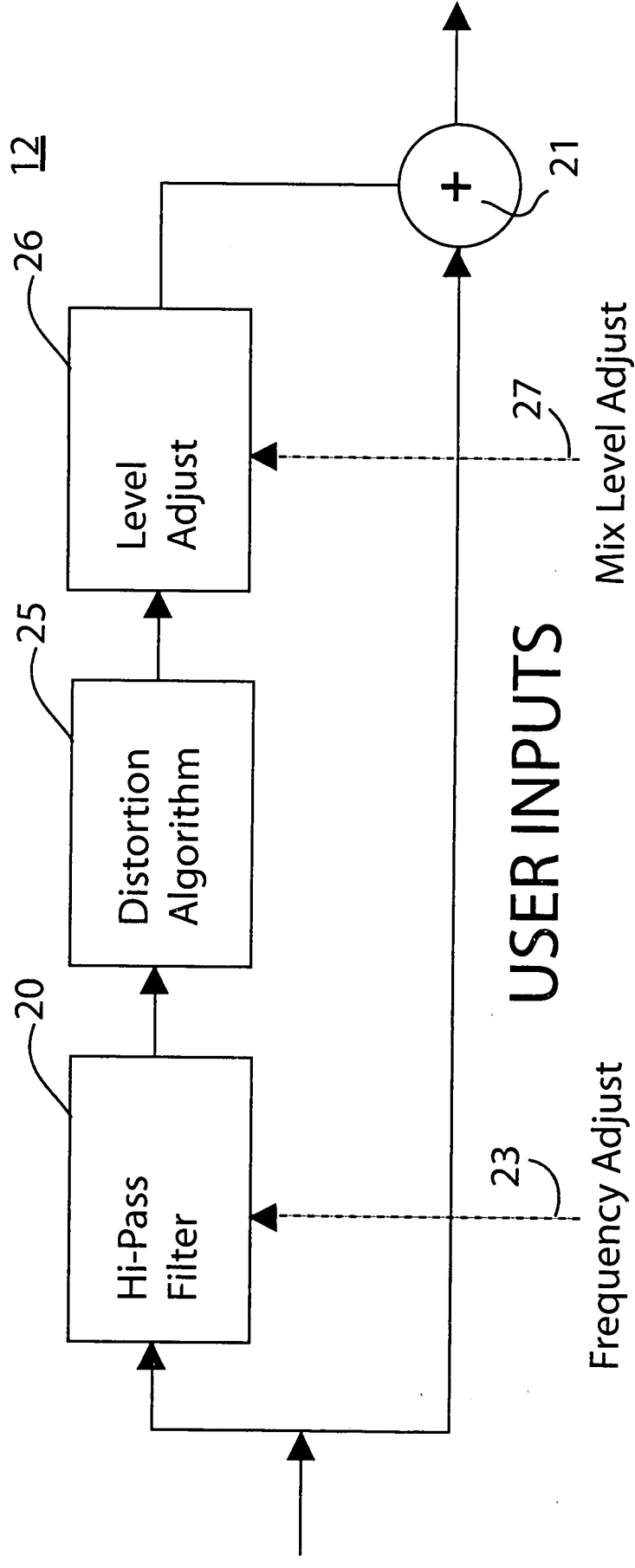


FIGURE 2

FIGURE 3

Warmth Adder

Transistor / IC

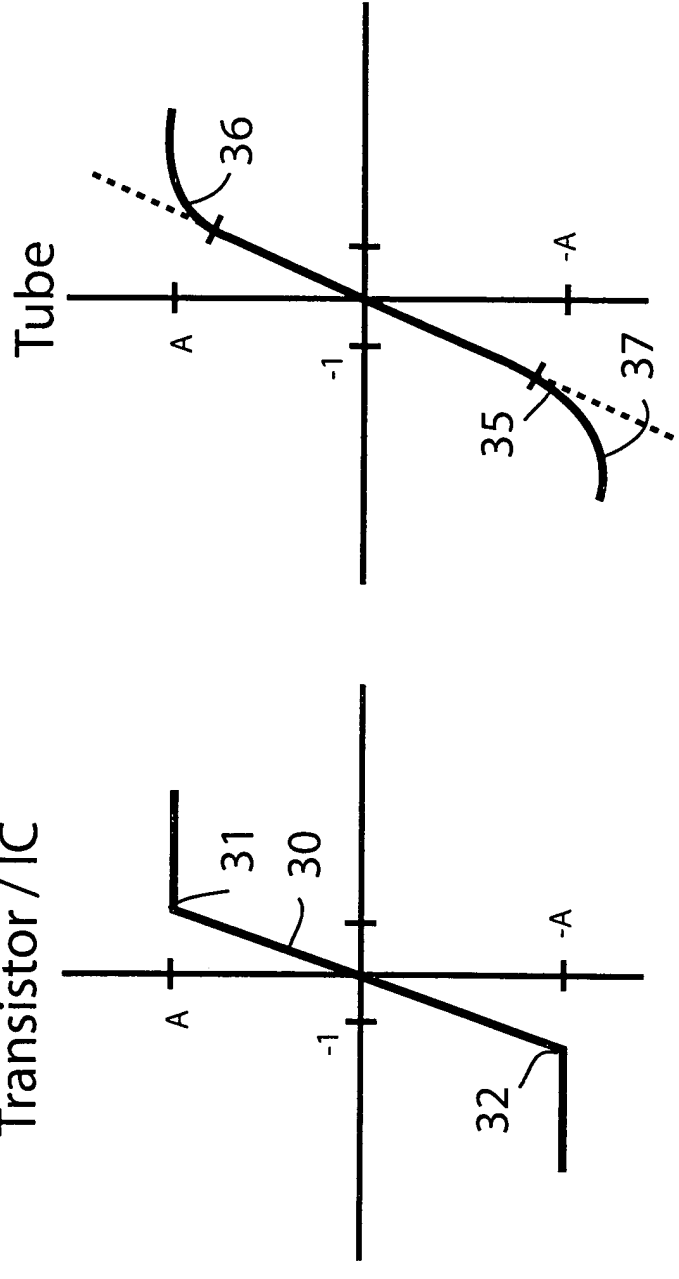
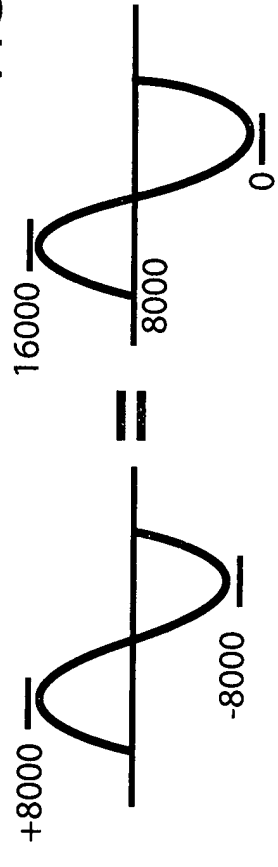


FIGURE 4



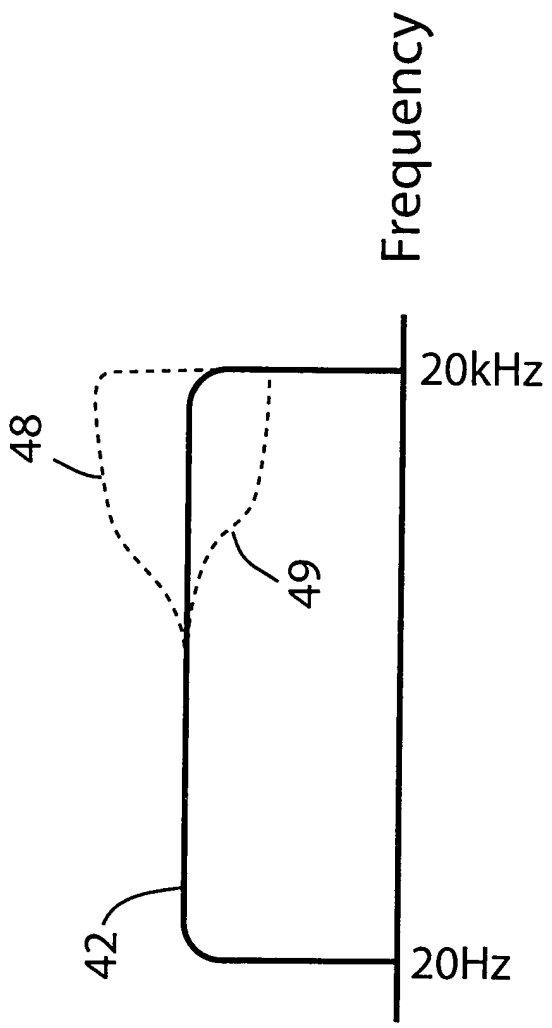


FIGURE 5

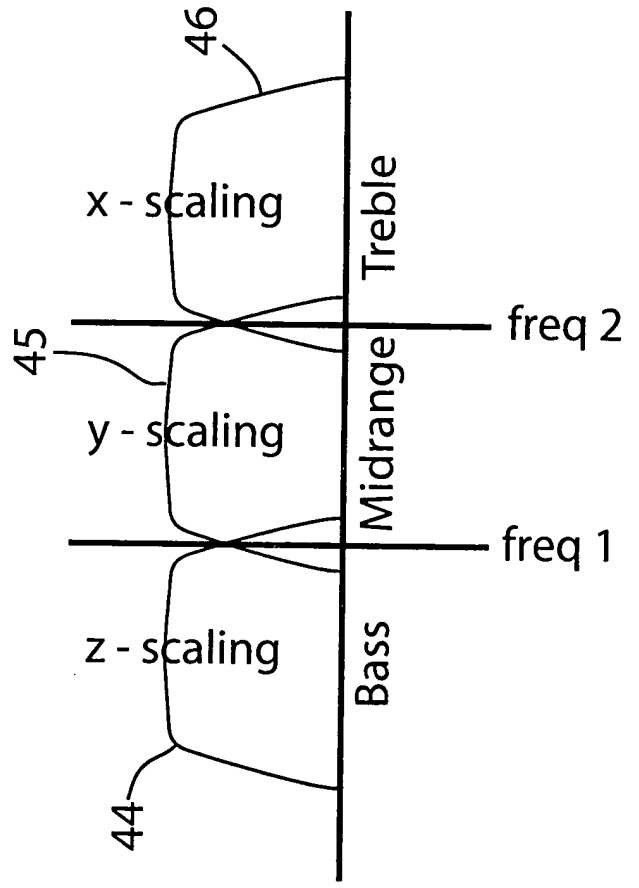


FIGURE 6

Processor Block Diagram

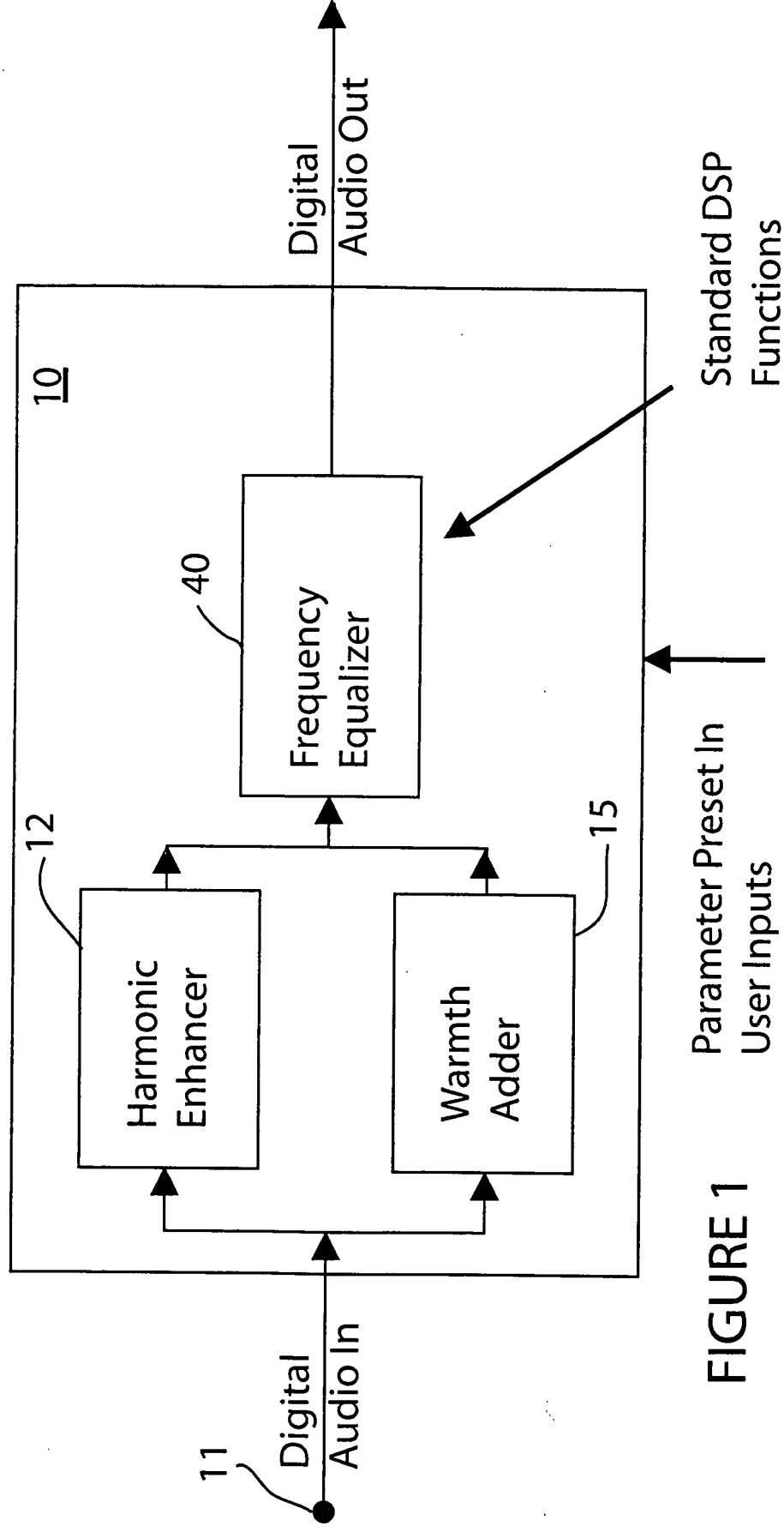


FIGURE 1

Harmonic Enhancer Diagram

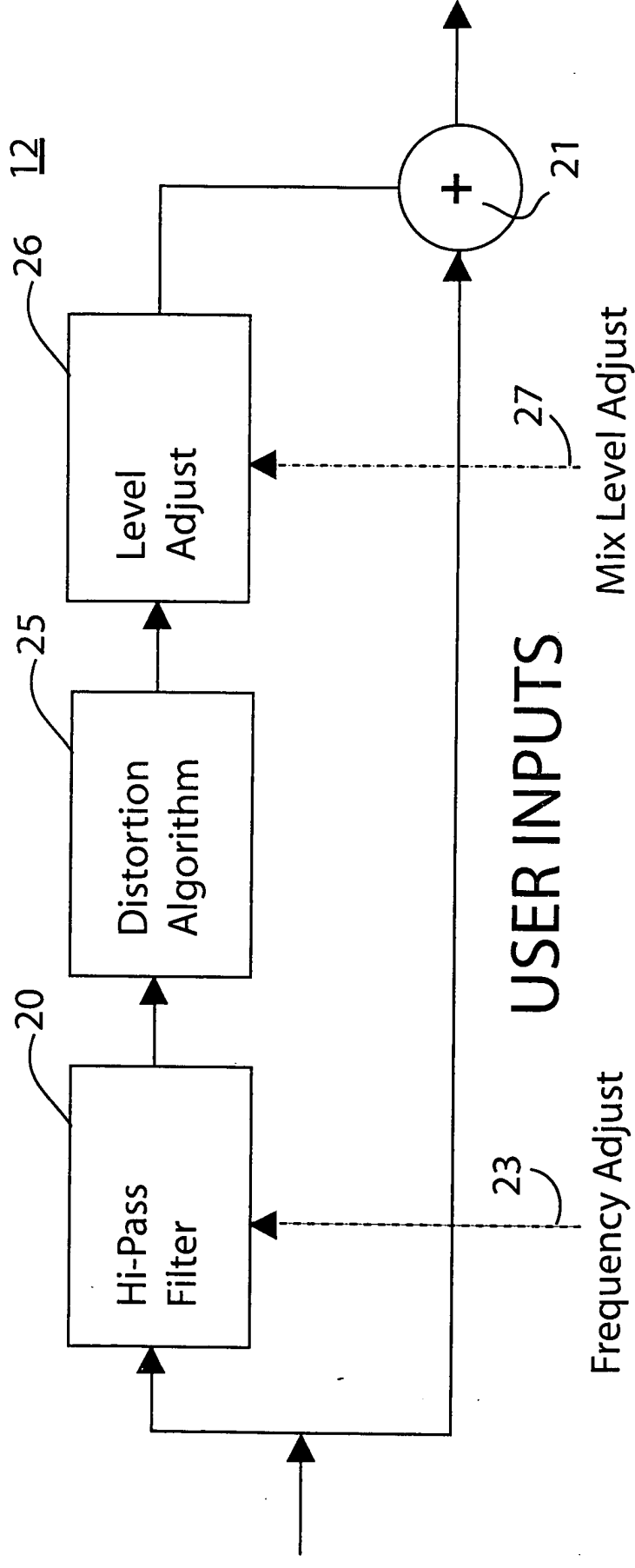


FIGURE 2

FIGURE 3

Warmth Adder

Transistor / IC

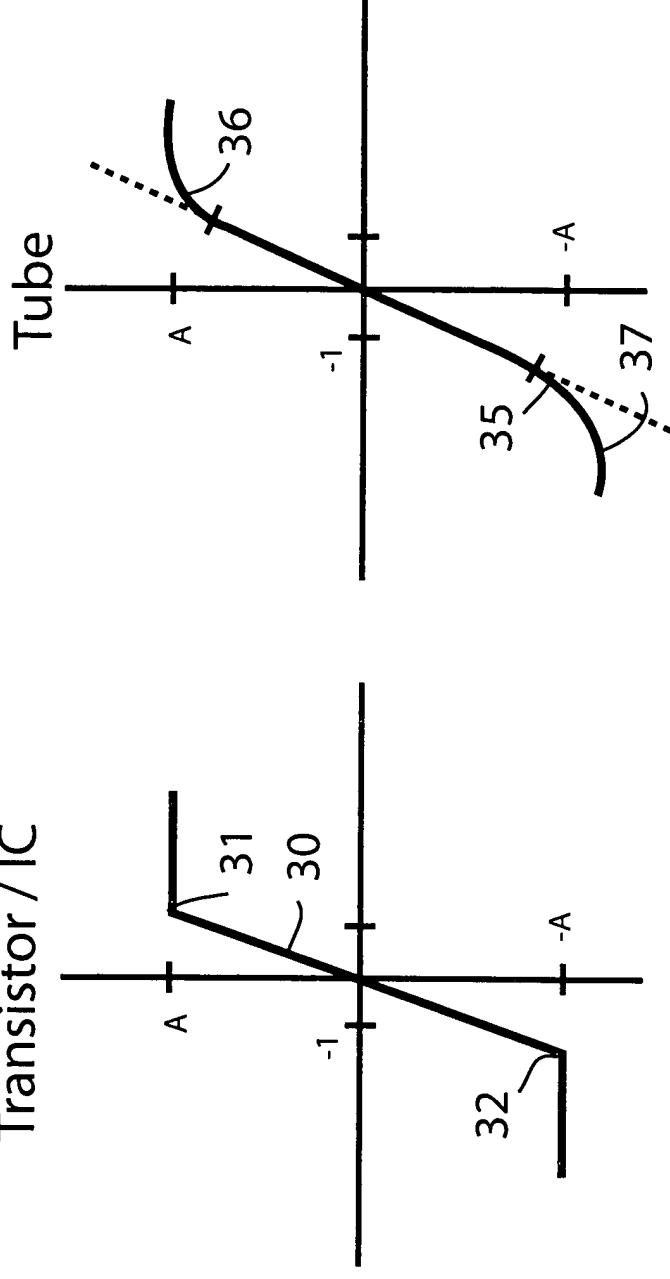
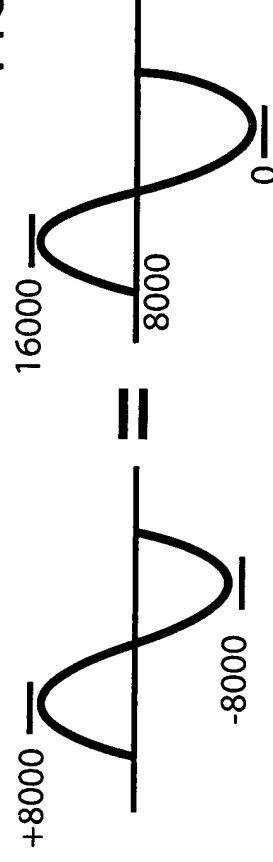


FIGURE 4



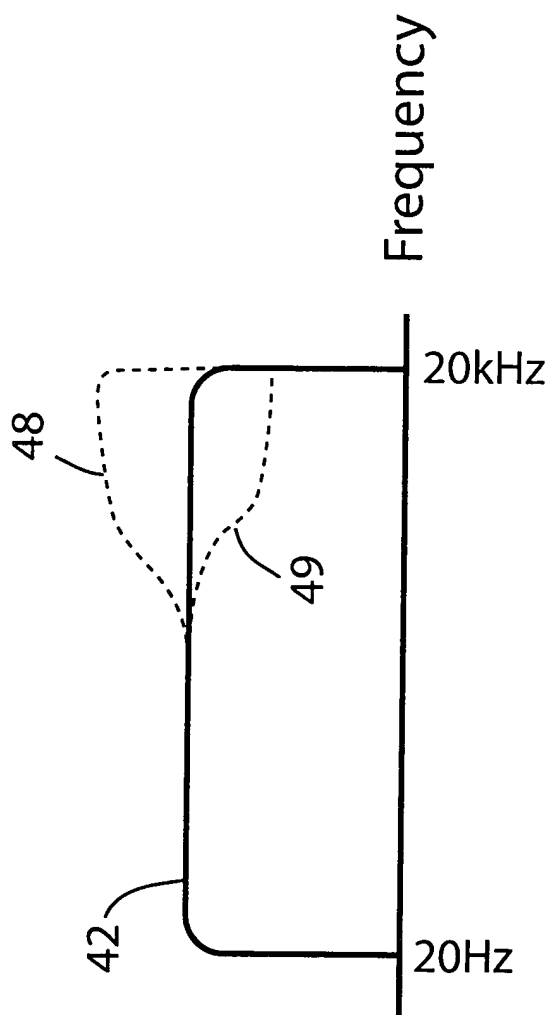


FIGURE 5

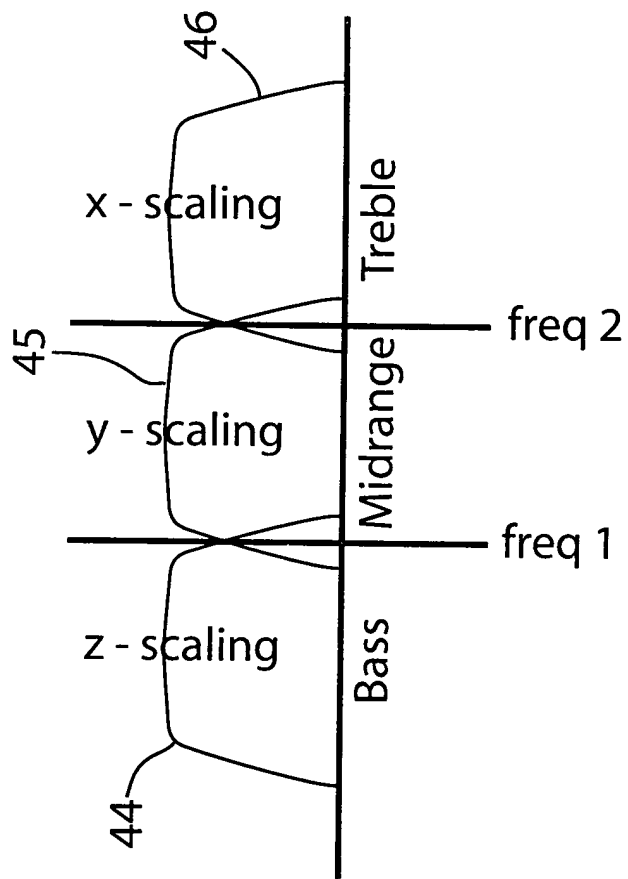


FIGURE 6